

C.V. Bart Dierickx

Born 21 August 1959, Mortsel, Belgium

Education, training

1978-1983 Burgerlijk Ingenieur E.-W., Electronics (MS EE) at the Catholic University of Leuven (K.U.Leuven). Promotor Prof. Willy Sansen.

1990 PhD in Applied sciences at the K.U.Leuven, On the subject *Transient phenomena and noise in CMOS at cryogenic temperatures*. Promotor: Prof. Gilbert Declerck.

Professional career

1983-1984: assistant professor at the K.U.Leuven, department ESAT, CCD-group.

1984-1999: IMEC (Leuven), ASP-division, CCD-group, since 1994 MAP-division, Microsystems, as leader of the optical sensor group

2000-2004: founder and VP R&D/CTO of FillFactory NV, Mechelen, Belgium

2004-2006: CTO & director of consumer design, Image sensor Division of Cypress Semiconductor, Mechelen

2006-2008: manager of the Technology Aware Design program, at IMEC, Leuven, Belgium.

December 2006: Founder and CTO of Caeleste CVBA, Antwerp, Belgium

May 2007: Gastprofessor at Vrije Universiteit Brussel (VUB) dept ETRO